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Applicant(s): Bryant et al.

Docket No.

BUR920010086US1

Serial No.
09/683,486Filing Date
1/7/2002Examiner
Gebremariam, Samuel A.Group Art Unit
2811Invention: FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH SPACER GATE AND
METHOD OF FABRICATTION**OFFICIAL****RECEIVED
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bryant *et al.*

Examiner: Gebremariam, Samuel A.

Serial No.: 09/683,486

Art Unit: 2811

Filed: 1/7/2002

For: **FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH
SPACER GATE AND METHOD OF FABRICATION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Final Office Action mailed September 10, 2003, please amend the
above-identified application as follows:

09/683,486

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